

HCQX-AD/DA04-D2 Hardware Instruction**AD04-D2**
DA04-D2

ManualNo.	HPP154000EN
Version	1.2
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1 Introduction

Thank you for purchasing and using the Q series analog modules independently developed and produced by KCFA Corporation. This manual will give the brief explanation for the following modules in the table:

Name	Module	Version	Power	Description
Analog input module	HCQX-AD04-D2	V1.00	1.2w	4 channel analog input. Connected to the local extension of main unit or the back of coupler, cannot be used alone. Support single-ended and differential input. Input range: -10V~+10V, 16bit resolution.
Analog output module	HCQX-DA04-D2	V1.00	1.2w	4 channel analog output. Connected to the local extension of main unit or the back of coupler, cannot be used alone. Support single-ended current/voltage output. 16bit resolution.

DANGER When the user selects modules according to the power, part of the power is reserved to avoid the loss during the signal transmission.

For the users of HCFA Q series analog modules, refer to this manual to perform the wiring, installation, diagnosis and maintenance required to the users to have the certain knowledge of electrical and automation.

This manual gives the necessary information for the use of HCFA Q series analog modules, please read this manual carefully before use and make the correct operation with full attention to safety.

1.1 Safety Precautions**1.1.1 Safety symbols**

DANGER	Indicates that incorrect handling may cause hazardous conditions, resulting in death or severe injury or significant property damage.
WARNING	Indicates that incorrect handling may cause hazardous conditions, resulting in minor or slight personal injury or physical damage.
CAUTION	Indicates that incorrect handling may cause slight injury or property damage.
NOTE	Indicates that incorrect handling may cause damage to the environment / equipment or data loss.

Key points or explanations to help with better operation and understanding of product.

1.1.2 Safety precautions**STARTUP AND MAINTENANCE PRECAUTIONS**

- Do not touch any terminal while the PLC's power is on. Doing so may cause fire, equipment failures, or malfunctions.
- Before cleaning or relighting terminals externally cut off all phases of the power supply. Failure to do so may cause electric shock.
- Before modifying or disrupting the program in operation or Forced output, RUN, STOP etc., carefully read through this manual and the associated manuals and ensure the safety of the operation. An operation error may damage the machinery or cause accidents.

STARTUP AND MAINTENANCE PRECAUTIONS

- Do not disassemble or modify the PLC. Doing so may cause fire, equipment failures, or malfunctions.
- For module repair, contact our KCFA distributor.

- Turn off the power to the PLC before connecting or disconnecting any extension cable. Failure to do so may cause equipment failures or malfunctions.
- Turn off the power to the PLC before attaching or detaching the following devices. Failure to do so may cause equipment failures or malfunctions.
 - Display module, peripheral devices, expansion boards
 - Extension blocks and special adapters
 - Battery, terminal block and memory cassette

DISPOSAL PRECAUTIONS

- Please contact a certified electronic waste disposal company for the environmentally safe recycling and disposal of your device.

TRANSPORT AND STORAGE PRECAUTIONS

- The PLC is a precision instrument. During transportation, avoid impacts larger than those specified in Section 3.1. Failure to do so may cause failures in the PLC. After transportation, verify the operations of the PLC.

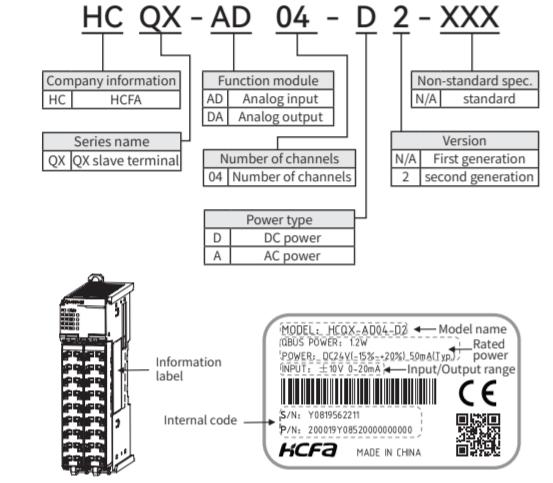
2 Product Overview**2.1 Model name description**

Figure 1 Model name and nameplate description

A

HCQX-AD/DA04-D2

Item**Description****STARTUP AND MAINTENANCE PRECAUTIONS**

- Describes the basic information about the product, such as the model and power.
- Describe the model's name.
- Describe the rated power
- QBUS POWER: Power consumption, Power consumption and current of QBUS
- Input/Output range: Range of input voltage and current INPUT: Range of output voltage and current OUTPUT: Range of output voltage and current
- Internal code: P/N: S/N: Internal code

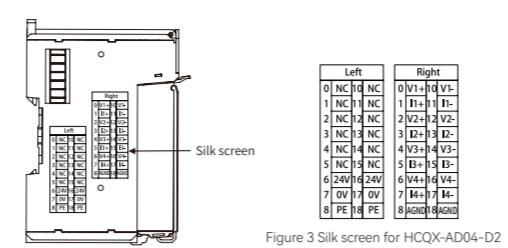
■ Terminal board arrangements

Figure 1 Silk screen for HCQX-AD04-D2

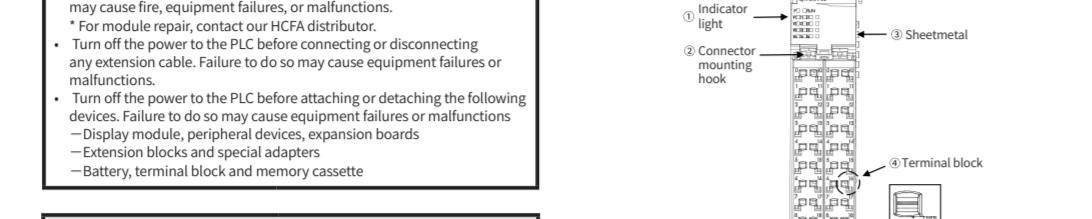
2.2 Part name description**2.2.1 HCQX-AD04-D2 Analog Input Module**

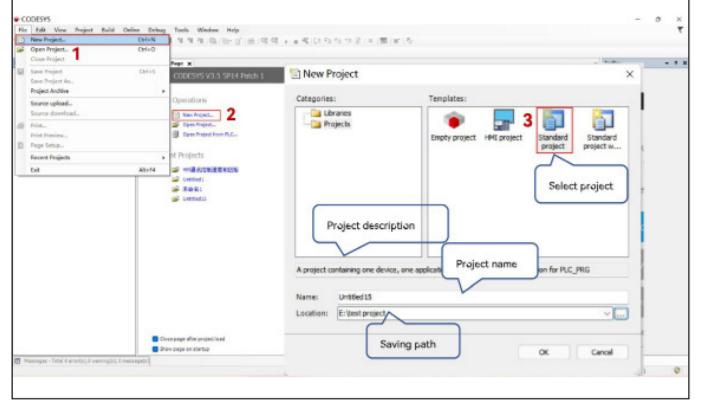
Figure 2 Interface diagram for HCQX-AD04-D2

2.3 Right terminal block description for HCQX-AD04-D2**2.3 Left terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Bottom terminal block description for HCQX-AD04-D2****2.3 Top terminal block description for HCQX-AD04-D2****2.3 Bottom terminal block description for HCQX-AD04-D2****2.3 Top terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Left terminal block description for HCQX-AD04-D2****2.3 Right terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Bottom terminal block description for HCQX-AD04-D2****2.3 Top terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Left terminal block description for HCQX-AD04-D2****2.3 Right terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Bottom terminal block description for HCQX-AD04-D2****2.3 Top terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Left terminal block description for HCQX-AD04-D2****2.3 Right terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Bottom terminal block description for HCQX-AD04-D2****2.3 Top terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Left terminal block description for HCQX-AD04-D2****2.3 Right terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Bottom terminal block description for HCQX-AD04-D2****2.3 Top terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Left terminal block description for HCQX-AD04-D2****2.3 Right terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Bottom terminal block description for HCQX-AD04-D2****2.3 Top terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Left terminal block description for HCQX-AD04-D2****2.3 Right terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Bottom terminal block description for HCQX-AD04-D2****2.3 Top terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Left terminal block description for HCQX-AD04-D2****2.3 Right terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Bottom terminal block description for HCQX-AD04-D2****2.3 Top terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Left terminal block description for HCQX-AD04-D2****2.3 Right terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Bottom terminal block description for HCQX-AD04-D2****2.3 Top terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Left terminal block description for HCQX-AD04-D2****2.3 Right terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Bottom terminal block description for HCQX-AD04-D2****2.3 Top terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Left terminal block description for HCQX-AD04-D2****2.3 Right terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Bottom terminal block description for HCQX-AD04-D2****2.3 Top terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Left terminal block description for HCQX-AD04-D2****2.3 Right terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Bottom terminal block description for HCQX-AD04-D2****2.3 Top terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Left terminal block description for HCQX-AD04-D2****2.3 Right terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Bottom terminal block description for HCQX-AD04-D2****2.3 Top terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Left terminal block description for HCQX-AD04-D2****2.3 Right terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Bottom terminal block description for HCQX-AD04-D2****2.3 Top terminal block description for HCQX-AD04-D2****2.3 Middle terminal block description for HCQX-AD04-D2****2.3 Left terminal block description for HCQX-**

5 Module programming examples

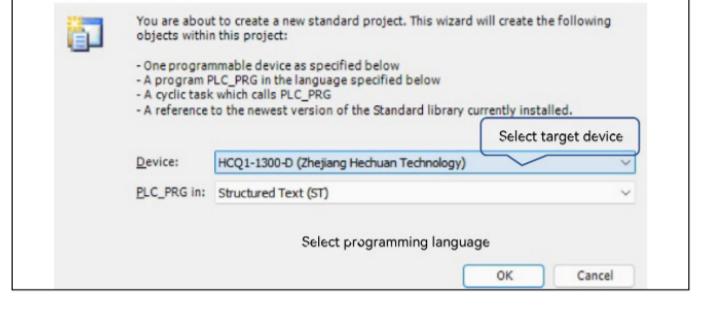
This example uses the CPU unit HCQ1-1300-D + coupler module HCQ1-EC + high-speed counter module HCQ1-HC04-D as an example to illustrate: Q1 connection has been described briefly here. For more details, refer to Q1 Software Manual.

1) Open CODESYS V3.5 SP14, select New project



The user can select the project type they want, enter the name and save path, and then click "OK".

2) Follow the CODESYS guide, select the target device and main program PLC_PRG programming language. Q1 device is not installed yet, so you need to install the device description file first, otherwise the correct target device cannot be selected.



You are about to create a new standard project. This wizard will create the following objects within the project:

- One programmable device as specified below
- A program PLC_PRG in the language specified below
- A cyclic task which calls PLC_PRG
- A reference to the newest version of the standard library currently installed.

Select target device

Device: HCQ1-1300-D (Zhejiang Jieduan Technology)

PLC_PRG in: Structured Text (ST)

Select programming language

OK Cancel

3) Double click Device → Scan network, then select the Q1 device and click "OK".



4) After communicating with Q1 device, click Device → Add device → EtherCAT Master SoftMotion.



5) Double click EtherCAT Master SoftMotion, and find the "Source Address (Mac)" under the "General" on the right and select the correct EtherCAT network card.



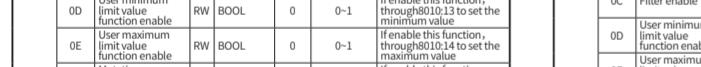
6) Right-click EtherCAT Master SoftMotion to select the scan device and for the module, which works normally and has established communication, find it in the "Scan device" and click the "Copy all devices to the project" in the lower right corner to add the module to the project.



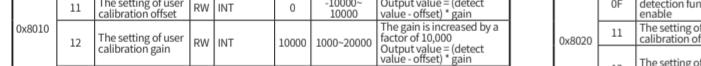
7) Use ST programming language to define two groups of INT variables in PLC_PRG, and map them to the corresponding input variables.



8) Map the two groups of channels of HCQ1-AD04-D to the variables iAnalogueInput1_V and iAnalogueInput1_A defined in the program, respectively, as follows:



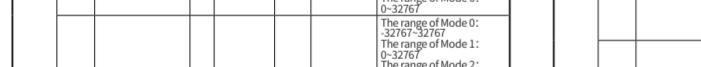
9) After no error for compiling, log in and run the program, and set the current channel operating mode to signal type to be measured. First, select the channel to be set, and then tick "Enable Expert Mode" in "General".



10) Set the working mode for the module under the CoE line page 16#0800:01 and confirm that the channel 16#0800:00 is turned ON (make sure that 24VDC is supplied normally). For more specific parameter setting, refer to Appendix: Description of the table of object dictionaries.



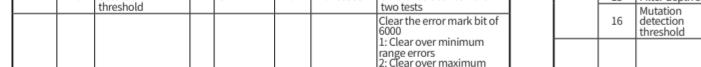
11) After the configuration completion, check the current input channel values under the EtherCAT I/O mapping.



12) Use ST programming language to define two groups of INT variables in PLC_PRG, and map them to the corresponding output variables.



13) After the configuration completion, check the current output channel values under the EtherCAT I/O mapping.



14) The setting of user calibration offset

15) The setting of maximum detection limit

16) Filter depth setting

17) Mutation detection threshold

18) Ch2 voltage calibration parameter

19) Factory calibration gain

20) Ch2 current calibration parameter

21) Factory calibration offset

22) Ch2 voltage calibration gain

23) Factory calibration offset

24) Ch2 current calibration gain

25) Factory calibration offset

26) Ch2 voltage calibration gain

27) Factory calibration offset

28) Ch2 current calibration gain

29) Factory calibration offset

30) Ch2 voltage calibration gain

31) Factory calibration offset

32) Ch2 current calibration gain

33) Factory calibration offset

34) Ch2 voltage calibration gain

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137) Factory calibration offset

138) Ch2 voltage calibration gain

139) Factory calibration offset

140) Ch2 current